

#25/H  
PB  
9/28/03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****In re Application of**

Chen, et al.

**Serial No.:** 09/748,256      **Group Art Unit:** 2811**Filed:** December 27, 2000      **Examiner:** Ori Nadav**For:** METHOD FOR FABRICATING COMPLEMENTARY METAL OXIDE  
SEMICONDUCTOR (CMOS) DEVICES ON A MIXED BULK AND SILICON-ON-  
INSULATOR (SOI) SUBSTRATEHonorable Commissioner of Patents  
Alexandria, VA 22313-1450**SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. § 1.111**

Sir:

In Supplement to the Amendment filed herein on August 20, 2003, and in further response to the Office Action dated May 20, 2003, please amend the above-identified application as follows:

**INTRODUCTORY COMMENTS**

Claims 29-39 and 41-57 are all the claims presently pending in the application. Claims 29, 41 and 45-46, and 57 been amended to more clearly define the invention. Claims are 29, 41 and 45-46 independent.